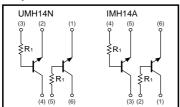
# General purpose (dual digital transistors) UMH14N / IMH14A

# Features

1) Two DTC144T chips in a UMT or SMT package.

# Equivalent circuit



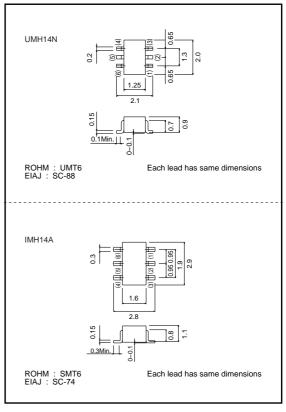
# •Package, marking, and packaging specifications

Туре	UMH14N	IMH14A	
Package	UMT6	SMT6	
Marking	H14	H14	
Code	TR	T108	
Basic ordering unit (pieces)	3000	3000	

#### •Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	50	V	
Collector-emitter voltage		Vceo	50	V	
Emitter-base voltage		Vebo	5	V	
Collector current		lc	100	mA	
Collector power dissipation	UMH14N	Pc	150(TOTAL)	mW	
	IMH14A	FU	300(TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

#### •External dimensions (Unit : mm)



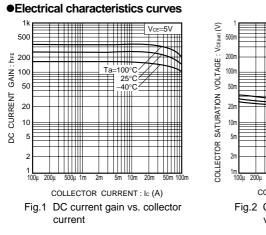
# •Electrical characteristics (Ta=25°C)

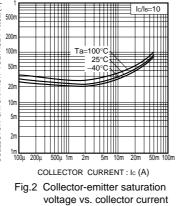
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	50	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	5	-	-	V	Ιε=50μΑ
Collector cutoff current	Ісво	-	-	0.5	μA	Vcb=50V
Emitter cutoff current	Іево	-	-	0.5	μA	VEB=4V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.3	V	Ic/I <sub>B</sub> =10mA/1mA
DC current transfer ratio	hfe	100	250	600	-	Vce/Ic=5V/1mA
Transition frequency	f⊤	-	250	-	MHz	Vce=10V, Ie=-5mA, f=100MHz *
Input resistance	R1	32.9	47	61.1	kΩ	-

\*Transition frequency of the device.

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# Transistors





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